



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/600,319
Filing Date	June 20, 2003
First Named Inventor	Mark J. Pavicic
Art Unit	2819
Examiner Name	Not Yet Known JEAN PIERRE
Attorney Docket Number	14676.01

Sheet 1 of 4

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
P/P		US- 2,600,193	6-10-1952	Bell, Jr., et al.	342/160
		US- 3,898,656	8-5-1975	Jensen	342/106
		US- 4,271,488	6-2-1981	Saxe	365/240
		US- 4,353,057	10-5-1982	Bernet et al.	340/347
		US- 4,811,285	3-7-1989	Walker et al.	365/45
		US- 4,833,445	5-23-1989	Buchele	341/118
		US- 4,922,452	5-1-1990	Larsen et al.	365/45
		US- 5,144,525	9-1-1992	Saxe et al.	365/45
		US- 5,257,025	10-26-1993	LeCroy, Jr.	341/100
		US- 5,406,507	4-11-1995	Knierim et al.	365/45
		US- 5,537,113	7-16-1996	Kawabata	341/141
		US- 5,796,361	8-18-1998	Levinson	341/172
		US- 6,091,619	7-18-2000	Kogan	365/45
P/P		US- 6,097,232	8-1-2000	McKinney	327/270

**FOREIGN PATENT DOCUMENTS**

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EXAMINER SIGNATURE

P. JEANPIERRE

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3/25/04

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PIP		G.M. Haller, "High-Speed, High-Resolution Analog Waveform Sampling in VLSI Technology", Ph.D. Dissertation, Technical Report No. ICL94-030, Sanford University, March 1994; pp. 83-88.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		G.M. Haller & B.A. Wooley, "A 700-MHz Switched-Capacitor Analog Waveform Sampling Circuit", IEEE J Solid-State Circuits, Vol. 29, No. 4, April 1994; pp. 500-508.	<input type="checkbox"/>	<input type="checkbox"/>
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PIP		F. Druillolle et al., "The Analogue Ring Sampler: An ASIC for the Front-End Electronics of the ANTARES Neutrino Telescope", IEEE Nuc Sci Sym, Nov 2001, P1-18.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		"Conceptual Design Report of the 0.1 km <sup>2</sup> Detector", ANTARES Internal Doc, Dec. 1999; pp. 27-28, 42-43.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		J.P. Denance, P. Nayman, F. Toussenel, & M. Punch, "Proposal for the HESS experiment electronics", LPNHE-Paris, HESS Electronics, Feb 1999; pp. 1-6.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		C. Bronnimann, R. Horisberger, & R. Schnyder, "The domino sampling chip: a 1.2 GHz waveform sampling CMOS chip", Nuc Inst & Meth in Phy Res, A420, 1999; pp. 264-269.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		S.A. Kleinfelder, "A Multi-Gigahertz Analog Transient Recorder Integrated Circuit", MS Thesis, UC-Berkeley, 1992; pp. 8-13.	<input type="checkbox"/>	<input type="checkbox"/>
PIP		S.A. Kleinfelder, "A Multi-GHz, Multi-Channel Transient Waveform Digitization Integrated Circuit", 2002, P1-6	<input type="checkbox"/>	<input type="checkbox"/>
PIP		G.T. Przybylski, "Analog Transient Waveform Digitizer R1.1 (V2)", LBNL, 2001. ( <a href="http://rust.lbl.gov/~gtp/ATWD/atwd_manual3.doc">http://rust.lbl.gov/~gtp/ATWD/atwd_manual3.doc</a> ). One page.	<input type="checkbox"/>	<input type="checkbox"/>

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R. JeanPierre

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Sheet

4

of

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			YES	NO
<i>PID</i>		J. Jacobson et al., "Technical Requirements, Engineering Solutions and R&D/Production Planning for the LBL Digital Optical Module", DOM Tech Design Report - LBNL, Dec 1998, <i>P 1-3</i>	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		D. Nygren, W. Greiman, D.M. Lowder, G. Przybylski, & R. Stokstad, "Digital Optical Module & System Design for a Km-Scale Neutrino Detector in Ice", LBNL-42013, Mar 1998; pp. 1-25.	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		Datasheet for Maxim MAX108 8-bit 1.5 GS/s ADC, Sept 1999. <i>pp. 1-2</i>	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		Datasheet for ATMEL TS83102G0B 10-bit GS/s ADC, June 2003, pp. 1-2.	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		W. O. LeCray, Jr., "Current-Sampling Waveform Acquisition For Digital Oscilloscope And Detector Applications", 5 <sup>th</sup> Int Conf on Calorimetry in High-Energy Physics, New York, 1994; pp. 436-441.	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		Y. Arai et al., "Development of Front-End Electronics for the Telescope Array Project", Int Cosmic Ray Conf, Aug 1999, <i>P. 1-4</i>	<input type="checkbox"/>	<input type="checkbox"/>
<i>PID</i>		"TMS320F28x DSP Analog-to-Digital Converter (ADC) Reference Guide", Texas Instruments, SPRU-060A, Aug 2003; pp. 1-1 - 1-4.	<input type="checkbox"/>	<input type="checkbox"/>
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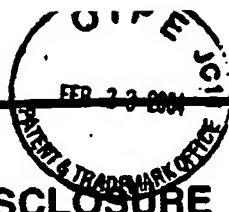
*P JEAN PIERRE*

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*3/25/04*

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Art Unit	2819
Examiner Name	Not Yet Known

Sheet	1	of	2	Attorney Docket Number	14676.01
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		US-			
		US-			
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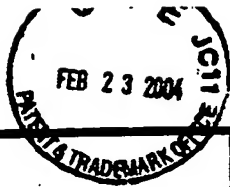
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		<b>Filing Date</b>	June 20, 2003		
		<b>First Named Inventor</b>	Mark J. Pavicic		
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<b>Sheet</b>	2	<b>of</b>	2	<b>Attorney Docket Number</b>	14676.01

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			YES	NO
PID		P. Barale, "STAR SCA/ADC Datasheet." September 1994.	<input type="checkbox"/>	<input type="checkbox"/>
PID		Datasheet for Acquiris waveform digitizer model D210, 500 MHz 2GS/s; June 1999; pp. 1-8.	<input type="checkbox"/>	<input type="checkbox"/>
PID		Datasheet for LeCroy PXD222 PXI Digitizers, July 2001.	<input type="checkbox"/>	<input type="checkbox"/>
PID		Advertisement for GaGe CompuScope 85GC, February 1, 2002.	<input type="checkbox"/>	<input type="checkbox"/>
PP		O. B. Milgrome, S. A. Kleinfelder, "A Monolithic CMOS 16 Channel, 12 Bit, 10 Microsecond Analog to Digital Converter Integrated Circuit", IEEE Trans Nuc Sci, Vol. 20, NO. 4, August 1993; pp. 721-723.	<input type="checkbox"/>	<input type="checkbox"/>
PID		S. A. Kleinfelder, "A 4096 Cell Switched Capacitor Analog Waveform Storage Integrated Circuit", IEEE Trans Nucl Sci, Vol. 37, No. 3, June 1990; pps. 1230-1236.	<input type="checkbox"/>	<input type="checkbox"/>
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PTO/SB/08A (10/01) (modified)  
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		US- 4,271,488	6-2-1981	Saxe	365/240
		US- 4,353,057	10-5-1982	Bernet et al.	341/136
		US- 4,811,285	3-7-1989	Walker et al.	365/45
		US- 4,833,445	5-23-1989	Buchele	341/118
		US- 4,922,452	5-1-1990	Larsen et al.	365/45
		US- 5,144,525	9-1-1992	Saxe et al.	365/45
		US- 5,257,025	10-26-1993	LeCroy, Jr.	341/100
		US- 5,406,507	4-11-1995	Knierim et al.	365/45
		US- 5,537,113	7-16-1996	Kawabata	341/141
		US- 5,796,361	8-18-1998	Levinson	341/172
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			YES	NO
PJP		R. Ball & D. Nitz, "A First Look at Switched Capacitor Array Technology", May 1996; pp. 1-11.	<input type="checkbox"/>	<input type="checkbox"/>
		M. Feuerstack-Raible, "Overview of microstrip read-out chips", Dec. 1999; pp. 1-14.	<input type="checkbox"/>	<input type="checkbox"/>
		F. Anghinolfi et al., "The ADAM Chip - Specification and Measurement Results", ATLAS Internal Note, INDET-113, Oct. 1995; pp. 1-5.	<input type="checkbox"/>	<input type="checkbox"/>
		S.R. Klein et al., "Front End Electronics for the STAR TPC", STAR Note #239, pp. 1-8. (Also published in IEEE Trans on Nuclear Science, v43, 1768 (1996)).	<input type="checkbox"/>	<input type="checkbox"/>
		M.S. Emery et al., "A Multi-Channel ADC for Use in the PHENIX Detector", IEEE Trans Nucl Sci, Vol. 44, No. 3, June 1997; pp. 374-378.	<input type="checkbox"/>	<input type="checkbox"/>
		M.S. Emery et al., "Timing and Control Requirements for a 32-Channel AMU-ADC ASIC for the PHENIX Detector", IEEE N5597 Nuc Sci Sym, New Mexico, Nov. 1997.	<input type="checkbox"/>	<input type="checkbox"/>
		F. Anghinolfi et al., "AROW - A 128 Channel Analogue Pipeline with Wilkenson ADC and Sparsification ASIC", The ATLAS Collaboration, 1995.	<input type="checkbox"/>	<input type="checkbox"/>
		T. Zimmerman et al., "SVX3: A deadtimeless readout chip for silicon strip detectors", Nuc Inst Meth, A409, 368 (1998).	<input type="checkbox"/>	<input type="checkbox"/>
		S. Nahn, "Status of CDF Tracking", slide with title "SVX3D ASIC", presentation at ICHEP, July 2002.	<input type="checkbox"/>	<input type="checkbox"/>
		"SVX4 Chip Specifications", May 2001; pp. 1-4.	<input type="checkbox"/>	<input type="checkbox"/>
PJP		L. Christofek et al., "SVX4 User's Manual", Version 7, May 2003; pp. 1-20.	<input type="checkbox"/>	<input type="checkbox"/>

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PTO/SB/08A (10/01) (modified)  
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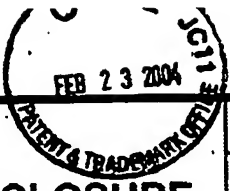
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PJP		"TMS320F28x DSP Analog-to-Digital Converter (ADC) Reference Guide", Texas Instruments, SPRU-060A, Aug 2003; pp. 1-1 - 1-4.	<input type="checkbox"/>	<input type="checkbox"/>
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on 4/6/2003

EXAMINER SIGNATURE P. Jean Pierre

DATE CONSIDERED 3/29/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/600,319
Filing Date	June 20, 2003
First Named Inventor	Mark J. Pavlicic
Art Unit	2819
Examiner Name	Not Yet Known

Sheet	1	of	1	Attorney Docket Number	14676.01
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**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
PJP		US- 5,416,480	5-16-1995	Roach et al.	341/61
PJP		US- 5,969,654	10-19-1999	Goldin	341/141
PJP		US- 6,255,962	7-3-2001	Tanenhaus et al.	340/870.05
		US-			
		US-			
		US-			
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**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Cite No.	FOREIGN PATENT DOCUMENT		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	TRANSLATION	
		Country Code:	Number - Kind Code (if known)				YES	NO
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P. JEANPIERRE

DATE CONSIDERED

3/29/04

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